

**College of Engineering Pune**  
 Computer Engg and IT(Tentative: 2-8Jan 2023)  
**Timetable For class: SY-Div1**

Batch Name

Room Name

Class Name

Subject Name

	08:00	09:00	10:00	11:00	12:00	13:00	14:00	15:00	16:00	17:00	18:00
Mo		SY Div 1 PLEVH AC-203	SY1-S1 DSL SY Div 1 DLD Lab SY1-S2 FOSS2 SY Div 1 DTL Lab SY1-S3 Cogni-1 SYDiv1 DSA I Lab SY1-S4 Cogni-2 SY Div 1 PPL Lab SY1-S5 Cogni-3 SY Div 1 FCS Tut	SY1-S5 ASL SY Div1 DSGT Tut	SY Div1 ODEMC AC-203	LUNCH	SY Div1 DLD AC-203	SY Div 1 FCS AC-203			
Tu			SY Div 1 PPL AC-203	LUNCH	SY Div1 ODEMC AC-201	SY1-S2 DSL SY Div 1 DLD Lab SY1-S3 FOSS2 SY Div 1 DTL Lab SY1-S4 Cogni-1 SYDiv1 DSA I Lab SY1-S5 Cogni-2 SY Div 1 PPL Lab SY1-S1 Cogni-3 SY Div 1 FCS Tut	SY1-S1 Cogni-3 SY Div1 DSGT Tut	SY Div1 DLD AC-203	SY1-S4 SY Div1 DSGT Tut AC-203		
We			SY1-S3 DSL SY Div 1 DLD Lab SY1-S4 FOSS2 SY Div 1 DTL Lab SY1-S5 Cogni-1 SYDiv1 DSA I Lab SY1-S1 Cogni-2 SY Div 1 PPL Lab SY1-S2 Cogni-3 SY Div 1 FCS Tut	SY1-S2 ASL SY Div1 DSGT Tut	SY Div 1 DSGT AC-203	LUNCH	SY Div1 DLD AC-203	SY Div 1 PPL AC-203			
Th			SY1-S4 DSL SY Div 1 DLD Lab SY1-S5 FOSS2 SY Div 1 DTL Lab SY1-S1 Cogni-1 SYDiv1 DSA I Lab SY1-S2 Cogni-2 SY Div 1 PPL Lab SY1-S3 Cogni-3 SY Div 1 FCS Tut	SY1-S3 Cogni-3 SY Div1 DSGT Tut	SY1-ODEMC-B2 GML SYDiv1 ODEMC Tut SY1-ODEMC-B4 ASL SYDiv1 ODEMC Tut SY1-ODEMC-B1 AC-203 SYDiv1 ODEMC Tut SY1-ODEMC-B3 PL SYDiv1 ODEMC Tut	LUNCH	SY Div 1 DSGT AC-203	SY Div 1 DSA-I AC-203			
Fri		SY1-S5 DSL SY Div 1 DLD Lab SY1-S4 Cogni-3 SY Div 1 FCS Tut		SY Div1 IC AC-203	SY Div 1 DTL AC-203	LUNCH	SY Div 1 DSA-I AC-203	SY1-S2 Cogni-4 SYDiv1 DSA I Lab SY1-S3 Cogni-5 SY Div 1 PPL Lab SY1-S1 Cogni-2 SY Div 1 DTL Lab	SY Div 1 PPL AC-203		

### Legend Page

Room Short Name	Room Name
AC-201	AC-201
GML	Graphics and Multimedia Lab
ASL	Advanced Software Lab
AC-203	AC-203
DSL	Digital Systems Lab
FOSS2	FOSS Lab-2
Cogni-1	Cognizant Lab-1
Cogni-4	Cognizant Lab-4
Cogni-2	Cognizant Lab-2
Cogni-5	Cognizant Lab-5
Cogni-3	Cognizant Lab-3
PL	Programming Lab

Subject Short Name: Batch Names	Teacher Name
SY Div1 ODEMC	Maths Faculty 1
SYDiv1 ODEMC Tut: SY1-ODEMC-B2	Maths Teacher1
SYDiv1 ODEMC Tut: SY1-ODEMC-B4	Maths Teacher 3
SY Div 1 PLEVH	Applied Science Teacher1
SY Div 1 DLD Lab: SY1-S1, SY1-S2, SY1-S3, SY1-S4	Pravin Revankar
SY Div 1 DTL Lab: SY1-S2, SY1-S4	Trishna Ugale
SY Div 1 DTL Lab: SY1-S3, SY1-S5	NewDTL Faculty
SYDiv1 DSA I Lab: SY1-S3, SY1-S4, SY1-S5, SY1-S1, SY1-S2	Pratiksha Deshmukh
SY Div 1 PPL Lab: SY1-S4	Vaibhav Khataavkar
SY Div 1 PPL Lab: SY1-S5, SY1-S3	Pradeep Deshmukh
SY Div 1 PPL Lab: SY1-S1, SY1-S2	Sumit Hirave
SY Div 1 FCS Tut: SY1-S5, SY1-S1, SY1-S2, SY1-S3, SY1-S4	Prof C Y Patil
SY Div1 DSGT Tut: SY1-S5, SY1-S1, SY1-S2, SY1-S3, SY1-S4	Rohini Sarode
SY Div 1 DLD Lab: SY1-S5	NewDLD Faculty
SY Div 1 DTL Lab: SY1-S1	new DTL faculty 2
SY Div1 DLD	Pravin Revankar
SY Div 1 DSA-I	Pratiksha Deshmukh
SY Div 1 PPL	Vaibhav Khataavkar
SY Div 1 DSGT	Rohini Sarode
SY Div 1 DTL	Trishna Ugale
SY Div 1 FCS	Prof C Y Patil
SYDiv1 ODEMC Tut: SY1-ODEMC-B1	Maths Faculty 1
SY Div1 IC	Applied Science Teacher1
SYDiv1 ODEMC Tut: SY1-ODEMC-B3	Maths Teacher 2

Subject Short Name	Subject Name
SY Div1 ODEMC	SY Div 1 Ordinary Differential Equations
SYDiv1 ODEMC Tut	SYDiv1 Ordinary Differential Equations
SY Div 1 PLEVH	SY Div1 Professional Laws Ethics and Values
SY Div 1 DLD Lab	SY Div 1 Digital Logic Design Lab
SY Div 1 DTL Lab	SY Div 1 Development Tools Laboratory
SYDiv1 DSA I Lab	SY Div 1 Data Structures and Algorithms I Lab
SY Div 1 PPL Lab	SY Div 1 Principles of Programming Languages Lab
SY Div 1 FCS Tut	SY Div 1 Feedback Control System Tut
SY Div1 DSGT Tut	SY Div 1 Discrete Structures and Graph Theory tut
SY Div1 DLD	SY Div 1 Digital Logic Design
SY Div 1 DSA-I	SY Div 1 Data Structures and Algorithms I
SY Div 1 PPL	SY Div 1 Principles of Programming Languages
SY Div 1 DSGT	SY Div 1 Discrete Structures and Graph Theory
SY Div 1 DTL	SY Div 1 Development Tools Labrotary
SY Div 1 FCS	SY Div 1 Feedback Control System
SY Div1 IC	SY Div 1 Innovation and Creativity

Timetable Incharge

Head of Department